



Indian Institute of Technology Indore

Discipline of Electrical Engineering  
Khandwa Road, Simrol, Indore

IIT Indore

Ref: IIT/EE/SKV/SMDP-C2SD/Recruitment/02

Date: July 7<sup>th</sup>, 2017

**Walk-in Interview**

Motivated and eligible candidates are requested to attend the **walk-in-interview** for the “**Lab Engineer**” post on **contract basis** for the sponsored research project “**Special Manpower Development Program- Chip to System Design (SMDP-C2SD)**” sponsored by DeitY, Govt. of India.

S.No.	Post Name (Number)	Qualifications	Consolidated Salary/Month
1.	Lab Engineer (01)	1 <sup>st</sup> class M.Tech. or equivalent in VLSI Design / VLSI Design Tools and Technology / VLSI Design and Nanoelctronics / Microelectronics & VLSI / Microelectronics / VLSI Design and Embedded System/ Embedded System / Electronics System Design / Automation and Robotics / Internet of Things/CSE/ECE/EI/EE and other relevant engineering areas with hand on experience on VLSI EDA/TCAD Tools / Embedded System Design / Hardware Design / Testing and knowledge of Linux operating system and server maintenance.	Rs. 34,000/-

**Duration:** Initial appointment for 12 months and can be extended till project completion based on performance and institute guidelines.

Candidates are requested to report at **9 am** for walk-in-interview on **29<sup>th</sup> July 2017 (Saturday)** in **Room No. 208 & 209, School Building, Simrol, IIT Indore**. Please bring two photographs, your CV, one set photocopy of all educational & experience documents. Further, please don't forget to bring your original documents for verification purposes. Please note that your CV should include contact details (address, phone no., email ID), date of birth, qualifications and experiences, project/research work and other relevant information.

No TA/DA will be paid for appearing in the interview.

Queries may be raised to Dr. S. K. Vishvakarma, Associate Professor, Electrical Engineering and Chief Investigator, SMDP-C2SD at his email id [skvishvakarma@iiti.ac.in](mailto:skvishvakarma@iiti.ac.in).