

**Adjunct Associate Professor in Electrical and Computer Science & Engineering,
IIT Indore**



Dr. Devesh Dwivedi has 18+ years of experience (14+ years of industry + 4 years of research). He holds a Ph. D. degree in Electronics Engineering (Microelectronics) from IIT BHU. Since October 2008, he is working with System and Technology Group, IBM. He worked on system-z & system-p projects. Presently he is senior manager, leading and managing ASIC OEM custom IP (High Speed Serial Links, Analog & Mixed Signal and Memory) design and

development at IBM India Pvt. Ltd, Bangalore, India. He started his carrier as SRAM circuit design engineer with ST Microelectronics in 1999. He has designed and delivered several complex and state-of-the-art memories (custom as well compilable) and custom IPs from specs to silicon, also has experience on H/W testing, silicon debug & customer facing .

Dr. Dwivedi has 10 granted/filled patents and 25 paper publications. He is member of technical review committees, patent and paper review board. He is examiner for graduate and post graduates thesis from Tier-1 technical institute and universities in India. He has delivered several invited talks, key notes at Institutes, Universities, National and International conferences. He is honored with Adjunct Faculty position at IIT and NIT. Dr Dwivedi supervised 6 M.Tech students (India and abroad Univ.) for master's dissertation work. He is supervising two PhD students from IIT. His student received prestigious IBM global PhD fellowship award in 2012-13 & 2013-14.

Adjunct Assistant Professor in Computer Science & Engineering, IIT Indore



Maitreya Natu received M.S. and Ph.D. degree in Computer and Information Sciences, both from the University of Delaware, DE, USA. He has done internships at Google and IBM research labs and is currently working as a Scientist in the Systems Research Lab at the Tata Research Design and Development Centre (TRDDC), Pune, India.

He has received the Best Research Award at IEEE Network and Operations Management Symposium and the Alan P. Colburn Award at the University of Delaware. He is an active researcher in systems and networking research community and has published more than 50 papers in international conferences and journals. He is on technical program committee and organizing committee of various national and international conferences. He is an adjunct faculty at the Indian Institute of Technology, Kanpur, and the Discipline of Computer Science, Indian Institute of Technology, Indore. His research interests include network management, network security, data analytics, and next-generation data centers.

Adjunct Associate Professor in Material Science and Engineering, IIT Indore



Dr. Suman Sinha Ray has finished his PhD in Mechanical Engineering from University of Illinois at Chicago in 2011. During his PhD studies, he was awarded several prestigious awards, namely- Dean's Fellowship, Chicago Consular Corps Scholarship, Provost award etc. He worked as Post Doctoral Research Associate in University of Illinois at Chicago from 2011-2013. In 2013 he joined United States Gypsum as Technology Scout.

He is also an Adjunct Assistant Professor in the Department of Mechanical and Industrial Engineering at University of Illinois at Chicago. He has co-authored 40 peer reviewed journal publications, multiple conference publications, one book chapter. He coadvised 5 PhD graduates and 11 undergraduate students. His research interests include Advanced Manufacturing, Experimental and Theoretical Thermal-Fluid Sciences at the Micro/Nanoscale, Polymer and Oxide Materials, Nano-structured Materials, Alternative Energy, Experimental and Theoretical Study of Production Methods of Nonwovens (Electrospinning, Solution Blowing and Melt Blowing).