

User Design Best Paper Award in VLSI Design 2016



Mr. Bhupendra Reniwal, PhD Scholar at [Nanoscale Devices, VLSI Circuit & System Design Lab](#), Discipline of Electrical Engineering working under supervision of Dr. S. K. Vishvakarma has received the **User Design Best Paper Award** in [“29th International Conference on VLSI Design and 15th International Conference on Embedded Systems, January 4-8, 2016, Kolkata, India.](#)



Mr. Bhupendra Reniwal receiving the award during the award ceremony function