



INDIAN INSTITUTE OF TECHNOLOGY INDORE
Department of Electrical Engineering
Simrol, Khandwa road, Indore, Madhya Pradesh, 453552 India

IIT INDORE

Email: vipul@iiti.ac.in; homepage: <https://www.iiti.ac.in/people/~vipul/>

Advertisement for JRF position under DST-SERB sponsored project

Department of Electrical Engineering, IIT Indore invites applications from highly motivated, sincere, hardworking and research-oriented individuals with strong analytical skills to apply for the position of Junior Research Fellow (JRF) in DST-SERB sponsored project titled “**Research and Development of Multiband UV Photodetectors Using Oxide Semiconductors**”, candidates are strongly advised to visit the [homepage of Dr. Vipul Singh](#) for ongoing research activities in his research group.

Details about the project: The project aims to achieve multiband UV photodetection using oxide semiconductors, through bandgap tuning and fine control of morphology of nanostructures of these oxide semiconductors. The work would involve growth of oxide semiconductor nanostructures and thin films, utilization of LSPR effect and tuning of bandgap of these semiconductors. This would further be utilized to realize multiband UV photodetection in the **UV-A, B and C** band of electromagnetic spectrum. **The project duration is for three years starting from 28/09/2021.**

Stipend & duration: The amount of fellowship will be as per DST norms for a period of one year (extendable based on performance review annually).

Education Qualifications: Qualifying degree **B.E./B.Tech.** in relevant areas of engineering or **M.Sc. in Physics/Electronics**, having qualified **GATE in PH/CS/EE/EC/IN or M.E./M.Tech.** in Electronics/ Electronics & communication Engineering/ Electrical/ Instrumentation & Control Engineering or any other relevant specialization with a first division as defined by the awarding Institute/University. **Candidates having prior experience in fabrication and characterization will be given preference.**

How to Apply: Interested candidates must send the following documents to Dr. Vipul Singh (vipul@iiti.ac.in) with the subject as “**Application for the post of JRF in DST-SERB project**” latest by **17:00 hrs, November 21, 2021. Interviews are scheduled on 22/11/2021.**

- Class 10th & 12th Marksheet
- Copy of UG &/or PG degree certificate (as applicable)
- Copy of GATE score card
- A detailed CV of the candidate with names of two referees

Only shortlisted candidates will be called for written and/or interview. Merely getting shortlisted does not entitle your selection as a JRF, and decision in this regard of the selection committee will be final and abiding. Moreover, IIT Indore reserves the right to fill or not to fill the post advertised. **Additionally note no TA/DA will be paid for attending the interviews.**

Contact details of PI:

Dr. Vipul Singh (Associate Professor), Department of Electrical Engineering, IIT Indore.

email: vipul@iiti.ac.in, Ph:+91-731-660-3274

NOTE: Possibility to be absorbed as Ph.D. student also exists.